


REMARKS

Further in response to the Official Action dated August 10, 2004, and as a supplement to the *Amendment* filed December 10, 2004, independent claims 55 and 57 have been amended to more clearly state the order of the transfer of the image signal. Specifically, claims 55 and 57 have been amended to recite a memory control circuit provided over a substrate and operationally connected to a memory portion (and a data line side driver circuit) where an image signal is transferred to the memory control circuit, from the memory control circuit to the memory portion, and from the memory portion to the data line side driver circuit. In addition to the arguments presented in the *Amendment* filed December 10, 2004, the Applicants further respectfully submit that Sato does not teach or suggest a memory control circuit provided over a substrate and operationally connected to a memory portion (and a data line side driver circuit) where an image signal is transferred to the memory control circuit, from the memory control circuit to the memory portion, and from the memory portion to the data line side driver circuit.

Should the Examiner believe that anything further would be desirable to place this application in better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number listed below.

Respectfully submitted,



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